-100mA / -50V Digital transistors (with built-in resistors)

DTA114YM / DTA114YE / DTA114YUA / DTA114YKA / DTA114YSA

Applications

Inverter, Interface, Driver

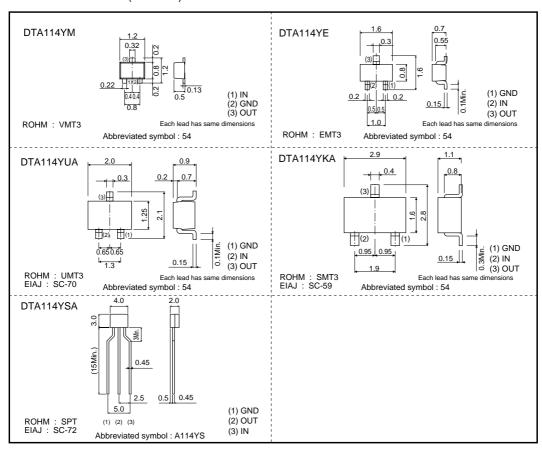
Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

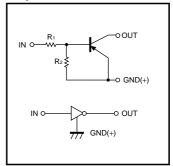
●External dimensions (Unit: mm)



Packaging specifications

	Package	VMT3	EMT3	UMT3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T146	TP
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTA114YM		0	-	-	-	_
DTA114YE		-	0	-	-	_
DTA114YUA				0		-
DTA114YKA		-		-	0	_
DTA114YSA		-	_	_	_	0

●Equivalent circuit



 $R_1{=}10k\Omega,~R_2{=}47k\Omega$

● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits DTA114YM DTA114YE DTA114YUA DTA114YKA DTA114YSA				
Supply voltage	Vcc	-50				V
Input voltage	Vı	-40 to +6				
Output ourrent	lo	-70				
Output current	IC(Max.)	-100				
Power dissipation	Pd	150 200		00	300	mW
Junction temperature	Tj	150				°C
Storage temperature	Tstg	−55 to +150				°C

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lanut valta sa	VI(off)	-	-	-0.3	.,	Vcc=-5V, Io=-100μA
Input voltage	VI(on)	-1.4	_	-	V	Vo=-0.3V, Io=-1mA
Output voltage	Vo(on)	-	-0.1	-0.3	V	lo/l≔–5mA/–0.25mA
Input current	lı	-	-	-0.88	mA	Vi=-5V
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V
DC current gain	Gı	68	-	-	-	Vo=-5V, Io=-5mA
Input resistance	R ₁	7	10	13	kΩ	_
Resistance ratio	R ₂ /R ₁	3.7	4.7	5.7	_	_
Transition frequency	f⊤ *	-	250	_	MHz	Vce=-10V, Ie=5mA, f=100MHz

^{*} Characteristics of built-in transistor

•Electrical characteristic curves

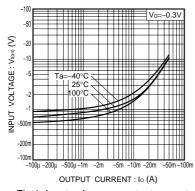


Fig.1 Input voltage vs. output current (ON characteristics)

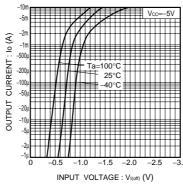


Fig.2 Output current vs. input voltage (OFF characteristics)

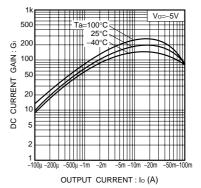


Fig.3 DC current gain vs. output

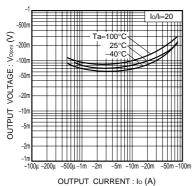


Fig.4 Output voltage vs. output current

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